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and Associated Methods"; U.S. Patent Application Serial No. 10/074,676, Attorney Docket No. SILA:098, titled "DC Offset Reduction in Radio-Frequency Apparatus and Associated Methods"; U.S. Patent Application Serial No. 10/075,094, Attorney Docket No. SILA:074, titled "Radio-Frequency Communication Apparatus and Associated Methods"; U.S. Patent Application Serial No. 10/075,098, Attorney Docket No. SILA:075, titled "Apparatus and Methods for Generating Radio Frequencies in Communication Circuitry"; U.S. Patent Application Serial No. 10/074,591, Attorney Docket No. SILA:096, titled "Apparatus for Generating Multiple Radio Frequencies in Communication Circuitry and Associated Methods"; and U.S. Patent Application Serial No. 10/079,058, Attorney Docket No. SILA:099, titled "Apparatus and Methods for Output Buffer Circuitry with Constant Output Power in Radio-Frequency Circuitry."

In the Claims:

Please cancel claim 2.

Please add claims 3-74.

The rewritten clean versions of all the pending claims are provided below. Attached at the end of this paper is an Appendix providing an indication of the changes relative to the prior version of the claims, as now required by Rule 121(c).

1. A front-end circuitry in a radio-frequency (RF) apparatus, comprising:
a filter circuitry with a differential output, having an output impedance, the filter circuitry configured to filter signals outside a signal band of interest; and
an impedance matching network, with a differential input coupled to the output of the filter circuitry, the impedance matching network further having a